

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10631921
<b>Filing Date:</b>	31-Jul-2003
<b>Title of Invention:</b>	TRANSISTOR GATE AND LOCAL INTERCONNECT
<b>First Named Inventor/Applicant Name:</b>	Jigish D. Trivedi
<b>Filer:</b>	James W. Ausley/Heather Clarke
<b>Attorney Docket Number:</b>	MICRON.104DV1

Filed as Large Entity

### Utility Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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**Basic Filing:**

**Pages:**

**Claims:**

**Miscellaneous-Filing:**

**Petition:**

**Patent-Appeals-and-Interference:**

**Post-Allowance-and-Post-Issuance:**

Certificate of correction	1811	1	100	100
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**Extension-of-Time:**

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>100</b>